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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): H.S. Nagesh et al.

Case: Serial No.: 10-26-12-10-40 10/785,352

Filing Date:

February 24, 2004

Group:

2661

Examiner:

To Be Assigned

Title:

Load Balancing Method and Apparatus for

Ethernet Over SONET and Other Types of Networks

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Attention: Official Draftsperson

Sir:

In response to the Notice to File Missing Parts dated May 17, 2004, Applicants submit herewith eight (8) sheets of formal drawings for the above-referenced patent application.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762 as required to correct the error.

Date: May 19, 2004

Respectfully submitted,

Joseph B. Ryan Reg. No. 37,922

Attorney for Applicant(s) Ryan, Mason & Lewis, LLP

90 Forest Avenue

Locust Valley, NY 11560

(516) 759-7517